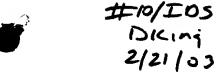
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ву: Д /

Date: January 28, 2003

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant

Harald Bachhofer et al.

Applic. No.

09/783,187

Filed

February 14, 2001

Title

Memory Configuration And Method For Reading A State From And

Storing A State In A Ferroelectric Transistor

Examiner

Roy Karl Potter

Art Unit: 28 22

## UNDER 37 C.F.R. 1.97(C)(2)

Hon. Commissioner of Patents and Trademarks, Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

United States Patent Application Publication US 2001/0038117 A1 (Haneder et al.), dated November 8, 2001;

German Patent DE 198 40 824 C1 (Haneder et al.), dated October 21, 1999, which corresponds with US 2001/0038117 A1;

International Publication WO 92/12518 (Krautschneider et al.), dated July 23, 1992, which corresponds with EP 0 566 585;

European Search Report dated May 29, 2001.

02/04/2003 JBALINAN 00000012 09783187

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If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and/or publications, it is because no existing translation is readily available to the applicant.

In accordance with 37 C.F.R. 1.97 (c) (2), consideration of this Information Disclosure Statement is requested.

Enclosed is the fee in the amount of \$180.00.

Respectfully submitted,

For Applicants

Date: January 28, 2003

Lerner And Greenberg, P.A. Post Office Box 2480 Hollywood, FL 33022-2480

Tel: (954) 925-1100 Fax: (954) 925-1101

/bmb

Mark P. Weichselbaum Reg. No. 43,248